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## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

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REVISED INFORMATION DISCLOSURE STATEMENT		Docket Number 2885/29	Technology Center 210		
Application Number 09/494,567	Filing Date January 31, 2000	Examiner Tonia L. Meonske	Art Unit 2183		
Invention Title RUN-TIME RECONFIGURABLE METHOD FOR PROGRAMMABLE UNITS		Inventor(s)  Martin VORBACH			

Address to:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Date: 7 MAYROM

Signature: Michelle M. Carridux

(Reg. No. 36,098)

## SIR:

- 1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. The references are listed on the attached modified PTO Form No. 1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- 2. The filing of this Information Disclosure Statement and the enclosed PTO Form No. 1449, shall not be construed as an admission that the information cited is prior art, or is considered to be material to patentability as defined in 37 C.F.R. § 1.56(b).
- 3. This Information Disclosure Statement is being filed to correct the defects in the Information Disclosure Statement filed on January 10, 2001, which was filed either (a) within three months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. 1.53(d), (b) within three months of the date of entry of the national stage as set forth in 37 C.F.R. 1.491 in an international application, (c) before the mailing date of a first Office Action on the merits in the present application, or (d) before the mailing of a first Office Action after filing of a request for continued examination. Accordingly, no certification or fee is required.
- 4. The references in the attached PTO Form 1449 that are indicated with an asterisk (\*) were submitted in the parent application, Patent Application Serial No. 08/946,998, filed on October 8, 1997,

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and are not included with this statement except with respect to the enclosed Foreign Patent Documents and Other Documents, and as indicated in Paragraphs 5, 6, and 7 below.

- 5. In the Office Action of July 24, 2003, the Examiner indicated that the previously filed Information Disclosure Statement was defective (37 CFR 1.98(a)(3)) in that Applicant did not include a concise explanation of the relevance of each patent listed that is not in the English language. Accordingly, Applicant encloses English abstracts of the following German language patents: DE 19651075 A1; DE 19654 846 A1; DE 44 16 881 C2; and DE 19704728 A1.
- 6. In the Office Action of July 24, 2003, the Examiner indicated that Applicant failed to comply with 37 CFR 1.98(a)(2) requiring a legible copy of each U.S. and foreign patent, each publication or that portion which caused it to be listed. Applicant submits the following complete copies of the following U.S. and foreign patents, indicated on the enclosed PTO Form 1449: U.S.P. 5,128,559; U.S.P. 5,142,469; U.S.P. 5,943,242; and EP 0 726 532 A2.
- 7. In the Office Action of July 24, 2003, the Examiner indicated that several U.S. and/or foreign patents had not been properly identified. Accordingly, Applicant has reviewed and revised where necessary, the identification of the patents listed at Paragraph 5 of the Detailed Action, Items a. ff.
- 8. In addition, Applicant noticed while reviewing the previously filed Information Disclosure Statement, that one non-patent reference that was disclosed in the parent application indicated above was inadvertently omitted. Accordingly, Applicant has included the following reference under "Other References:" Tau, E. et al., "A First Generation DPGA Implementation," FPP '95, pp. 138-143.
- 9. Accordingly, Applicant respectfully requests that the references in this Information Disclosure Statement, previously filed on January 10, 2001, be officially considered and made of record in the above-identified patent application.
- 10. It is believed that no fees are due in connection with this Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account No. 11-0600 for such fees. A copy of this communication is enclosed for charging purposes.

Dated: 1 Moyzwy

By:

Michelle M. Carniaux

(Reg. No. 36,098)

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INFORMATION DISCONSURE STATEMENT BY APPLICANT	APPLICANT Vorbach et al.		
	FILING DATE January 31, 2000	GROUP 2787	

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	Re. 34,363*	August 31, 1993	Freeman			
	4,489,857	February 6, 1996	Agrawal et al.			
	4,591,979	May 27, 1986	Iwashita	_		
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	4,761,755*	August 2, 1988	Ardini Jr. et al.	M	AY 1 1 2 <del>ology Ce</del> n	p04
	4,811,214*	Mar. 7, 1989	Nosenchuck et al.	Tooba	Noay Cen	er 2100
	4,852,048	July 25, 1989	Morton	[ COIIII	blogy con	
	4,870,302*	September 26, 1989	Freeman			
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, , ,	4,967,340*	October 30, 1990	Dawes			
	5,014,193*	May 7, 1991	Garner et al.			
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	Agrawal et al.	Aug. 3, 1993	5,233,539*
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	Magar et al.	Арг. 12, 1994	5,303,172*
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			Winters et al.	Jul. 2, 1996	5,532,693*
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	*Myers, G., Advances in Computer Architecture, Wiley-Interscience Publication, 2nd ed., John Wiley & Sons, Inc. P 463-94, 1978.				
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	*Maxfield, C. "Logic that Mutates While-U-Wait" EDN, The Design Magazine of the Engineering Industry (Bur. Ed) (USA), EDN (European Edition), 7 November 1996, Cahners Publishing, USA				
• 1	*M. Morris Mano, "Digital Design," by Prentice Hall, Inc., Englewood Cliffs, New Jersey, 1984, pp. 119-125, 154-161.				
	Norman, Richard S., "Hyperchip Business Summary - The Opportunity," Business Summary, http://www.hyperchip.com/business_summary.htm January 31, 2000, pages 1-3.				
	*Tau, E. et al., "A First Generation DPGA Implementation," FPP '95, pp. 138-143.				

EXAMINER	DATE CONSIDERED		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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